

Amendments to the Specification

Please replace the paragraph beginning on p. 15, line 9 with the following:

The second time delay TD2 may be measured by, but not limited to, a timer.

Please replace the paragraph beginning on p. 16, line 19 with the following:

The packet time delay may be measured by, but not limited to, a timer.

Please replace the paragraph beginning on p. 27, line 12 with the following:

The NIC 1200 of FIG. 12 corresponds to the method of generating interrupts 400 of FIG. 4. The first time delay estimating circuit 1220 estimates a first time delay in response to received data frames DFB. The interrupt INTS is generated after the first time delay has passed from when the data frames DFB are received. Thus, all the data frames DFB, which are received before the interrupt INTS is generated and after a first data frame is received, can be processed by an interrupt INTS. Therefore, the first time delay has to be longer than the time necessary for receiving one data frame DFB. The first time delay estimating circuit 1220 can include a timer, which measures a predetermined time in response to data frames DFB. However, those skilled in the art can understand that the first time delay estimating circuit 1220 can be another device and is not limited to the timer. The first time delay estimating circuit 1220 stops estimating the time delay if the interrupt INTS is generated.

Please replace the paragraph beginning on p. 30, line 1 with the following:

The second time delay estimating circuit 1230 can include a timer similar to the first time delay estimating circuit 1220. However, those skilled in the art can understand

that the second time delay estimating circuit 1230 can be another device and is not limited to the timer.